

Effect of incorporating two gate insulators (SiO₂ and Al₂O₃) on the performance of the a-IGZO-based TFT transistor

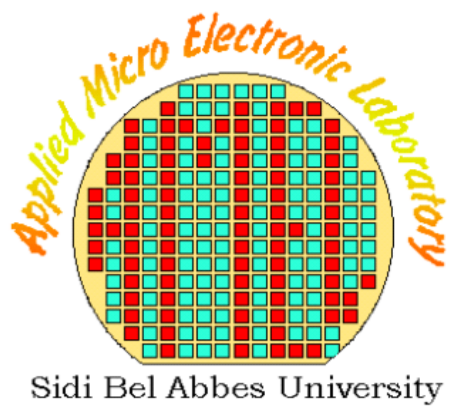
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Abstract: In this work we presented the simulation of the effect of the deep defects created by hydrogen on the performances of the thin film transistor of amorphous indium gallium zinc oxide (a-IGZO TFT) by incorporating two gate insulators Al₂O₃ and SiO₂. The results obtained are compared with those obtained by a-IGZO TFT - SiO₂.

INTRODUCTION

Metal oxides talc indium gallium oxide Amorphous zinc (a-IGZO) is a new promising for the development of thin film transistor because of their high electronic mobility, their low threshold voltage (V_{th}), high surface stability and low temperature manufacturing process [1,2]. In this study, we studied the effect of defects near-valence band (E_v) [3] on the output parameters of the a-IGZO TFT using the TCAD. a detailed numerical simulation is performed to understand the effect of defects near MBV on the performance of a-IGZO TFT with two gate insulators (Al₂O₃ and SiO₂).

Physical model by SILVACO-ATLAS

A 2D inverted-staggered a-IGZO TFT structure has been defined in this work which is illustrated in Figure 1. It consists of an active layer a-IGZO (20 nm thick), two insulators layers, the first is alumina (Al₂O₃) with a thickness of 50 nm and the second is silicon dioxide (SiO₂) have the same thickness as alumina (50 nm) and a silicon wafer substrate (n++) as gate. The length (L) and width (W) of the channel are 30 and 180 μm respectively. The source and the drain have a thickness of 5 nm made up of titanium (Ti).

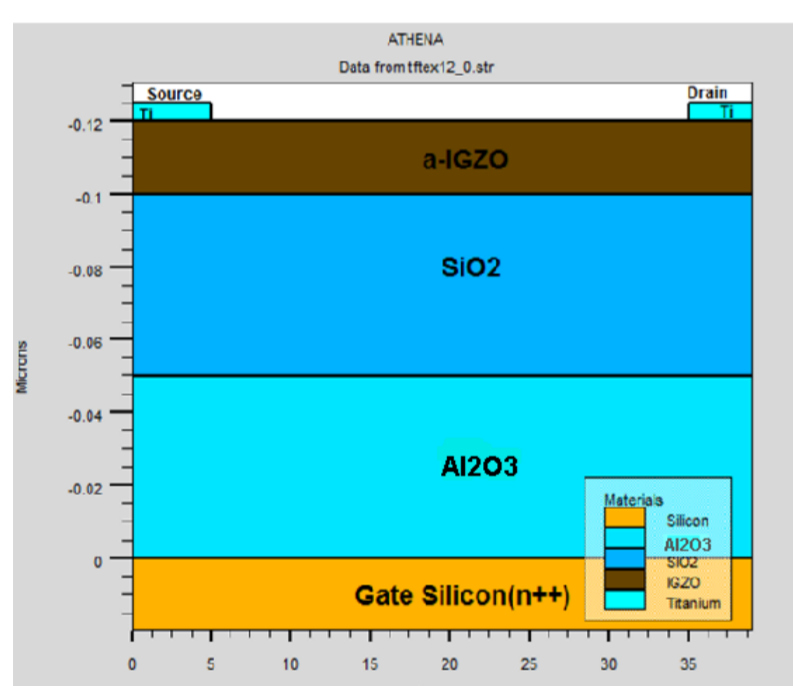


Figure 1: 2D inverted-staggered a-IGZO TFT structure with two gate insulators Al₂O₃ and SiO₂.

Result and discussion

we have 4 types of DOS for donor and acceptor type defects[4].

$$g_{vt}^D(E) = N_{td} \exp\left(\frac{E_v - E}{W_{td}}\right), g_{ct}^A(E) = N_{ta} \exp\left(\frac{E - E_c}{W_{ta}}\right), g_c^D(E) = N_{gd} \exp\left(\frac{-(E - E_{gd})^2}{W_{gd}}\right),$$

$$g_c^A(E) = N_{ga} \exp\left(\frac{-(E - E_{ga})^2}{W_{ga}}\right)$$

Effect of the tail donor density (Ntd)

Effect of the decay energy (Wtd)

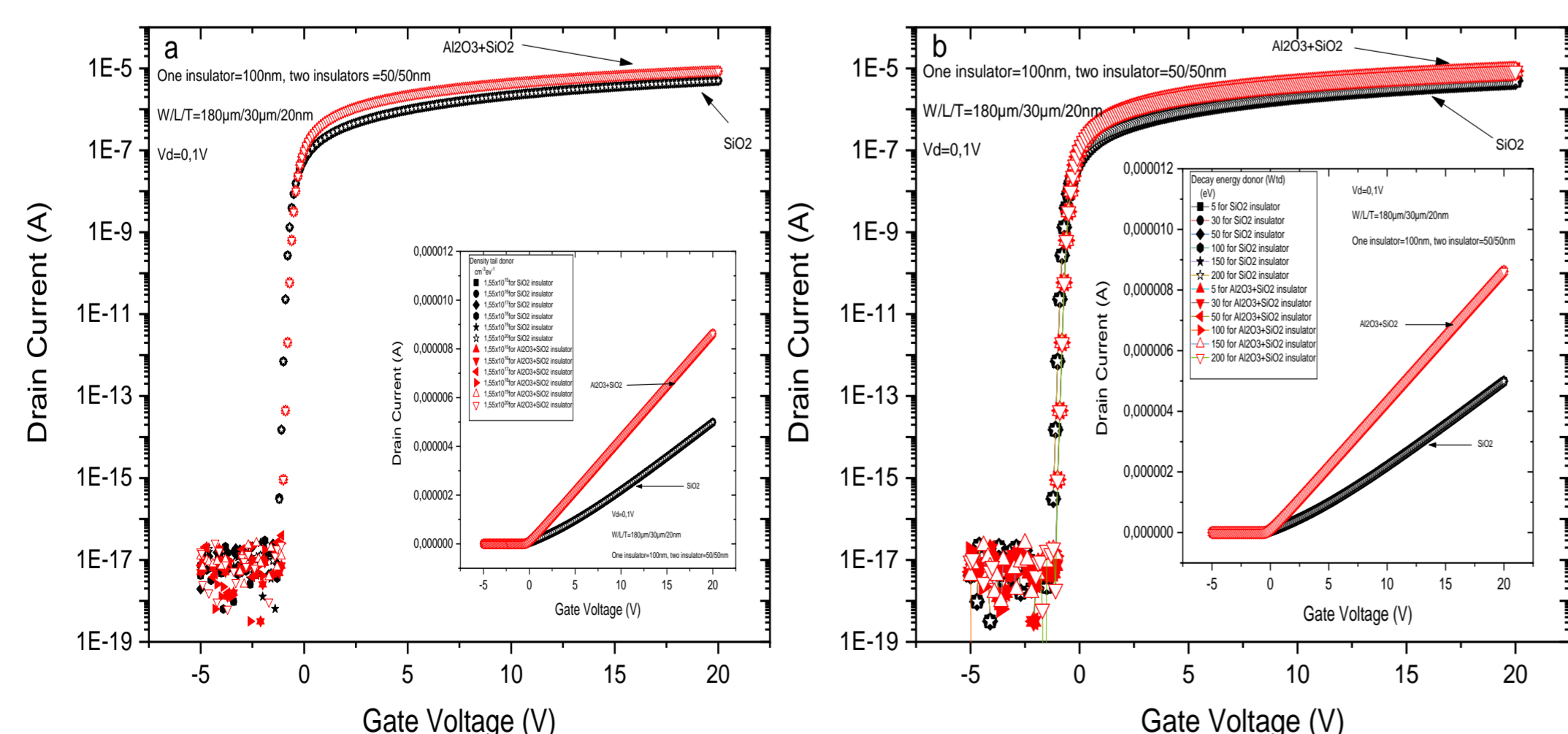


Figure 2: The effect of a) the donor tail densities, b) decay energy on the TFT transfer characteristics with two gate insulators (Al₂O₃+SiO₂).

Effect of the Gaussian acceptor density (Nga)

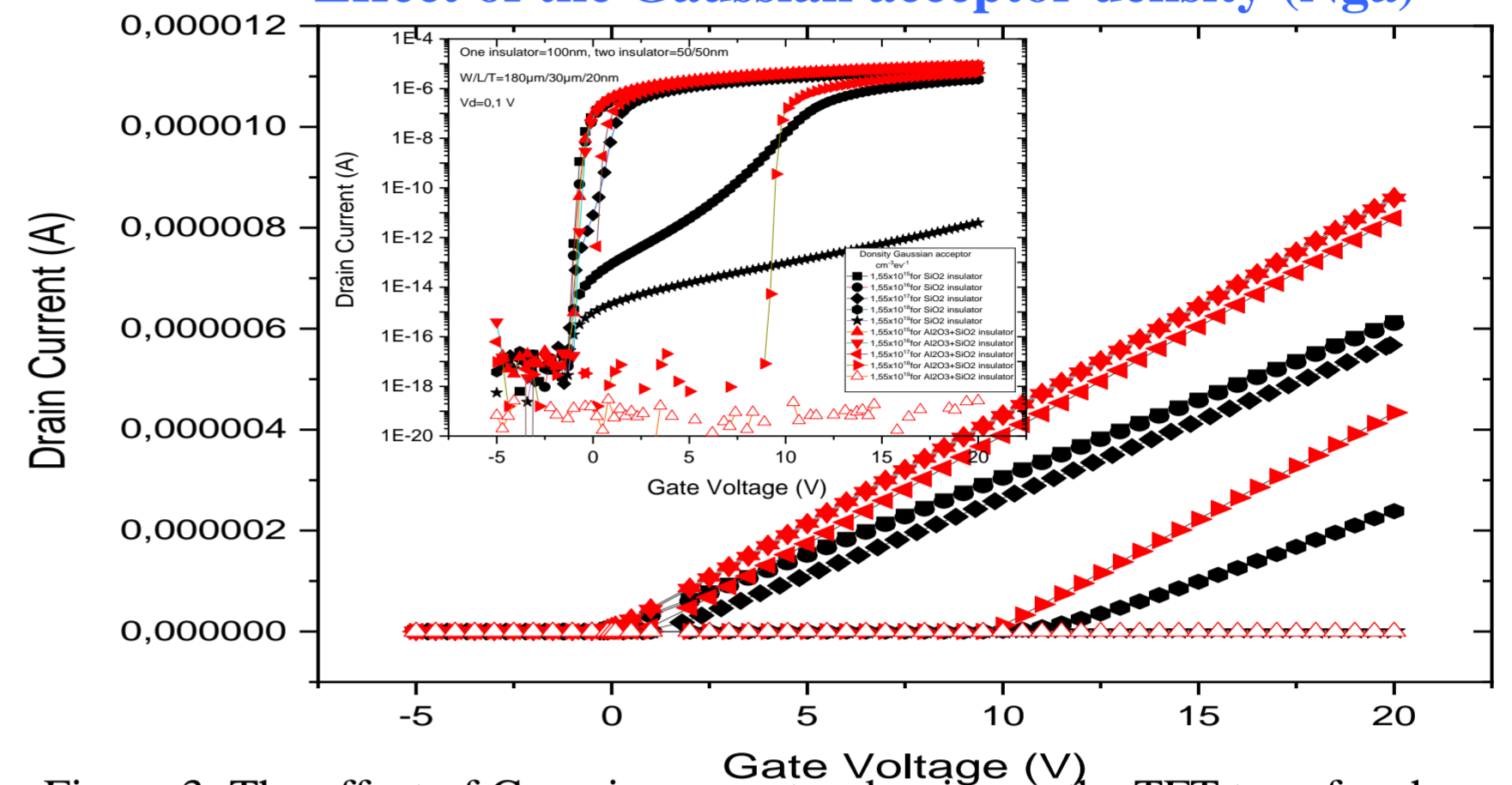


Figure 3: The effect of Gaussian acceptor density on the TFT transfer characteristics

Effect of the Gaussian acceptor energy width (Wga)

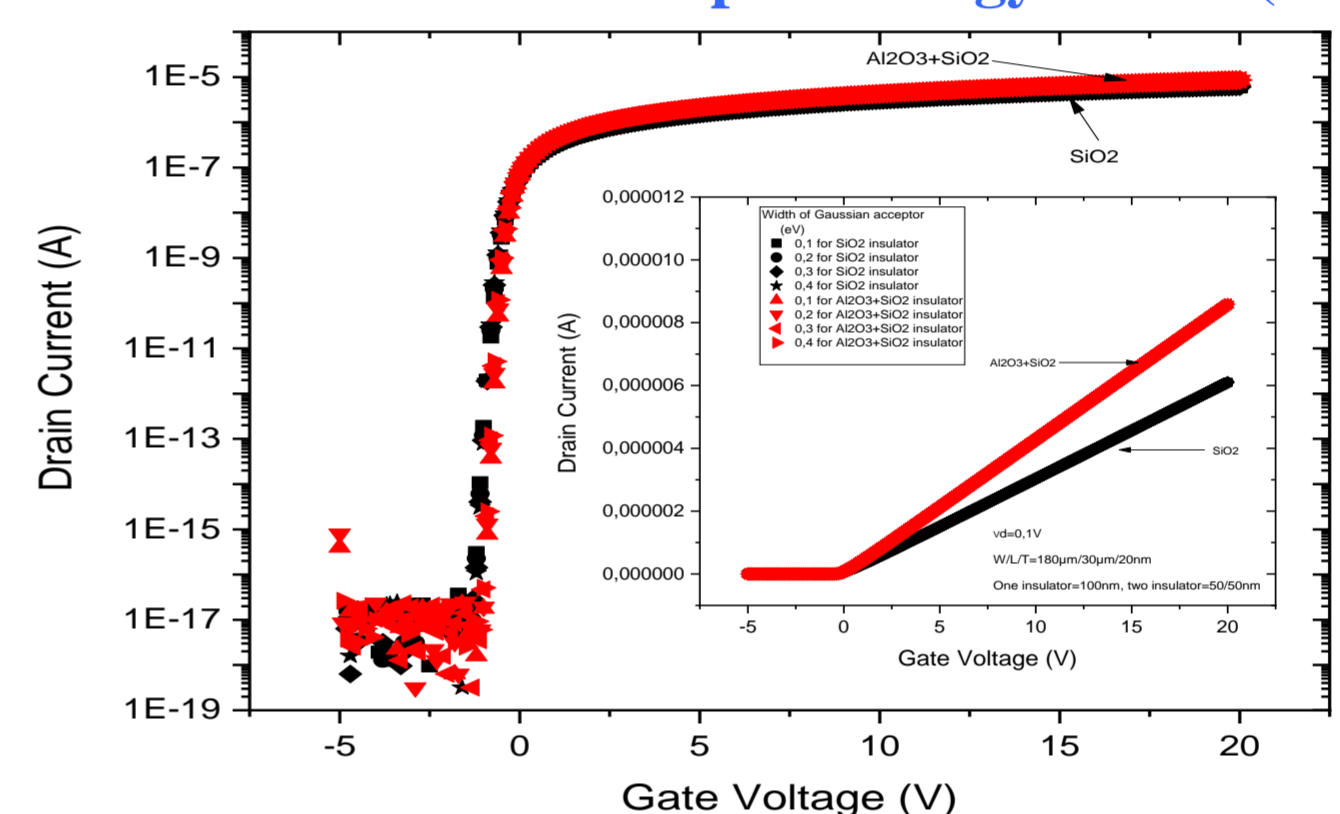


Figure 4: The effect of Gaussian acceptor width on the TFT transfer characteristics

Output parameter s	Two gate insulators Al ₂ O ₃ and SiO ₂				One gate insulator SiO ₂			
	Ntd= 1.55x10 ¹⁵ (cm ⁻³ eV ⁻¹)	Wtd=5 (meV)	Nga= 1.55x10 ¹⁸ (cm ⁻³ eV ⁻¹)	Wga=0.1 (eV)	Ntd= 1.55x10 ¹⁵ (cm ⁻³ eV ⁻¹)	Wtd=5 (meV)	Nga= 1.55x10 ¹⁸ (cm ⁻³ eV ⁻¹)	Wga=0.1 (eV)
Ioff (A)	3.23 x 10 ⁻¹⁹	3.23x10 ⁻¹⁹	1.58 x 10 ⁻¹⁹	2.22x10 ⁻¹⁸	9.54x10 ⁻¹⁹	3.23x10 ⁻¹⁸	2.23 x 10 ⁻¹⁸	9.54x10 ⁻¹⁹
Ion (A)	8.7x10 ⁻⁶	8.7 x 10 ⁻⁶	4.33 x 10 ⁻⁶	8.57 x 10 ⁻⁶	5.01x10 ⁻⁶	5.01 x 10 ⁻⁶	2.39x10 ⁻⁶	6.16x10 ⁻⁶
Ion/Ioff	2.69 x 10 ¹³	2.69 x 10 ¹³	2.74 x 10 ¹²	3.86 x 10 ¹²	5.24x10 ¹²	1.55 x 10 ¹²	1.07 x 10 ¹²	6.46x10 ¹²
Vth (V)	-0.01	-0.01	9.73	0.08	2.38	2.39	11.50	0.10
μeff (cm²V⁻¹s⁻¹)	14.9	14.9	14.7	14.9	13.8	13.7	13.5	13.7
SS (V/dec)	0.113	0.10	0.992	0.104	0.15	0.14	1.61	0.130

Table 1: Comparison of the TFT a-IGZO output parameters to two Al₂O₃+ SiO₂ gate insulators compared to a single reference gate insulator SiO₂.

Conclusion

We concluded that defects close to the valence band are not donor defects but acceptor defects with a Gaussian distribution which can also degrade mobility. On the other hand we can improve the output values of a-IGZO TFT by stacked insulator gate structure of Al₂O₃ and SiO₂ based on this mechanism, an optimized stacked GI structure that exhibited a low subthreshold swing and high μ_{eff} was found and used to achieve low-voltage operation in a TFT device. The new improve values of the output parameters: μ_{eff}, V_{th}, the Ion / Ioff and SS are given respectively: 14.9 cm²/ Vs, -0.01 V, 2.69x10¹³ and 0.11 V/dec.

References

- [1] K. Nomura, T. Kamiya, H. Yanagi, E. Ikenaga, K. Yang, K. Kobayashi, M. Hirano, H. Hosono, Subgap states in transparent amorphous oxide semiconductor, InGaZnO, observed by bulk sensitive x-ray photoelectron spectroscopy, Applied Physics Letters 92(20).
- [2] K. Nomura, T. Kamiya, E. Ikenaga, H. Yanagi, K. Kobayashi, H. Hosono, Depth analysis of subgap electronic states in amorphous oxide semiconductor, a-InGaZnO, studied by hard x-ray photoelectron spectroscopy, Journal of Applied Physics 109(7) 3560769.
- [3] W. T. Chen, S. Y. Lo, S. C. Kao, H. W. Zan, C. C. Tsai, J. H. Lin, C. H. Fang, C. C. Lee, Oxygen-dependent in stability and annealing/passivation effects in amorphous In-Ga-Zn-O thin-film transistors, IEEE Electron Device Letters 32(11)(2011)1552–1554.
- [4] T. C. Fung, C. S. Chuang, C. Chen, K. Abe, R. Cottle, M. Townsend, H. Kumomi, J. Kanicki, Two-dimensional numerical simulation of radio frequency sputter amorphous In-Ga-Zn-O thin-film transistors, Journal of Applied Physics 106(8) (2009)1–10.